

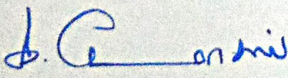
BOARD EXAMINATIONS PRACTICAL OCTOBER 2024
111, GOVERNMENT POLYTECHNIC COLLEGE, COIMBATORE - 14.

W-6

TIME TABLE

SUBJECT CODE & NAME	1052233320 - DIGITAL LOGIC DESIGN LAB
P.CODE	111-1052-1052233320
BRANCH / SEMESTER	COMPUTER ENGINEERING / III SEMESTER
TIME DURATION	3 HOURS
MAX MARKS	100

BATCH NO.	DATE & TIME	REGISTER NUMBER			TOTAL NO OF CANDIDATES
1	06.12.2024 9:30 AM – 12:30 PM	24500519	24500520	24500521	27
		24500522	24500524	24500525	
		24500527	24500530	24500531	
		24500532	24500534	24500535	
		24500536	24500538	24500539	
		24500540	24500541	24500542	
		24500544	24500545	24500546	
		24500549	24500550	24500551	
		24500552	24500553	24500554	
2	06.12.2024 01:00 PM – 04:00 PM	24500557	24500558	24500559	26
		24500561	24500562	24500563	
		24500564	24500565	24500567	
		24500568	24500569	24500570	
		24500572	24500573	24500574	
		24500575	24590159	24590160	
		24590161	24590162	24590163	
		24590164	24590165	24590166	
		24590167	24590168	***	
TOTAL					53


 INTERNAL EXAMINER
 (K MOSES, SID:11115201)


 HOD / CSE


 CHIEF SUPERINTENDENT
 CHIEF SUPERINTENDENT
 GOVERNMENT POLYTECHNIC COLLEGE,
 COIMBATORE-641 014